

N-Channel Enhancement Mode Field Effect Transistor

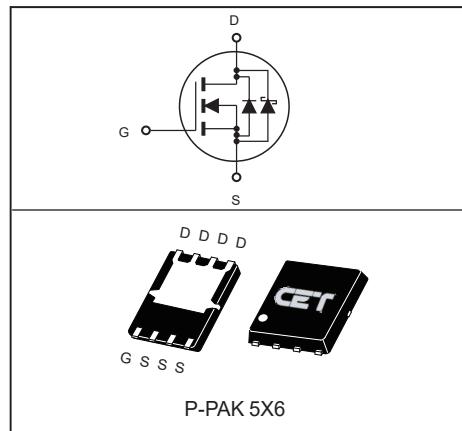
FEATURES

- High power and current handing capability.
- Reliable and rugged.
- Pb-free lead plating ; RoHS compliant.
- Halogen Free.
- Surface mount Package.
- Fast reverse recovery time.

APPLICATIONS

- DC to DC Converters.
- DC FAN
- Load Switch.

V_{DSS}	$R_{DS(ON)\text{ typ}}$	I_D	$@V_{GS}$
30V	2mΩ	102A	10V



ABSOLUTE MAXIMUM RATINGS

$T_C = 25^\circ\text{C}$ unless otherwise noted



Parameter	Symbol	Limit	Units
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	$I_D @ R_{\theta JC}$	$T_C = 25^\circ\text{C}$	A
		$T_C = 70^\circ\text{C}$	A
	$I_D @ R_{\theta JA}$	$T_A = 25^\circ\text{C}$	A
		$T_A = 70^\circ\text{C}$	A
Drain Current-Pulsed ^a	$I_{DM} @ R_{\theta JC}$	$T_C = 25^\circ\text{C}$	A
	$I_{DM} @ R_{\theta JA}$	$T_A = 25^\circ\text{C}$	A
Maximum Power Dissipation	P_D	41	W
Single Pulsed Avalanche Energy ^d	E_{AS}	144	mJ
Single Pulsed Avalanche Current ^d	I_{AS}	17	A
Operating and Store Temperature Range	T_J, T_{stg}	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Limit	Units
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	3	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	20	$^\circ\text{C}/\text{W}$



CEZ25C3

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 24\text{V}, V_{\text{GS}} = 0\text{V}$			0.5	mA
Gate Body Leakage Current, Forward	I_{GSSF}	$V_{\text{GS}} = 20\text{V}, V_{\text{DS}} = 0\text{V}$			100	nA
Gate Body Leakage Current, Reverse	I_{GSSR}	$V_{\text{GS}} = -20\text{V}, V_{\text{DS}} = 0\text{V}$			-100	nA
On Characteristics ^b						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}} = V_{\text{DS}}, I_D = 250\mu\text{A}$	1		3	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		2	2.5	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 20\text{A}$		2.7	3.5	$\text{m}\Omega$
Gate input resistance	R_g	f=1MHz,open Drain		3.3		Ω
Dynamic Characteristics ^c						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 15\text{V}, V_{\text{GS}} = 0\text{V}, f = 1.0 \text{ MHz}$		1465		pF
Output Capacitance	C_{oss}			1025		pF
Reverse Transfer Capacitance	C_{rss}			90		pF
Switching Characteristics ^c						
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 15\text{V}, I_D = 20\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		14		ns
Turn-On Rise Time	t_r			10		ns
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$			39		ns
Turn-Off Fall Time	t_f			8		ns
Total Gate Charge	Q_g	$V_{\text{DS}} = 15\text{V}, I_D = 20\text{A}, V_{\text{GS}} = 4.5\text{V}$		10.8		nC
Gate-Source Charge	Q_{gs}			2.6		nC
Gate-Drain Charge	Q_{gd}			4.5		nC
Drain-Source Diode Characteristics and Maximum Ratings						
Drain-Source Diode Forward Current	I_S				34	A
Drain-Source Diode Forward Voltage ^b	V_{SD}	$V_{\text{GS}} = 0\text{V}, I_S = 20\text{A}$			1.2	V
Reverse Recovery Time	T_{rr}	$I_F = 20\text{A}, di/dt = 100\text{A/us}$		31		ns
Reverse Recovery Charge	Q_{rr}			11.8		nC

Notes :

a.Repetitive Rating : Pulse width limited by maximum junction temperature.

b.Pulse Test : Pulse Width $\leq 300\mu\text{s}$. Duty Cycle $\leq 2\%$.

c.Guaranteed by design, not subject to production testing.

d.L = 1mH, $I_{AS} = 17\text{A}$, $V_{DD} = 25\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$

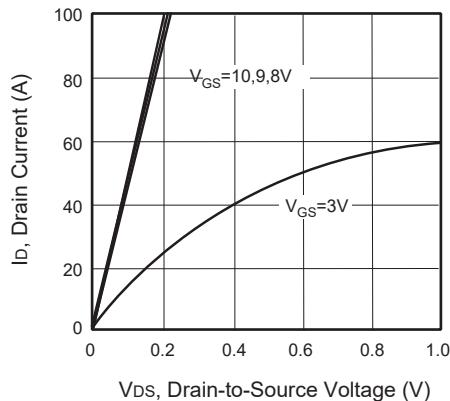


Figure 1. Output Characteristics

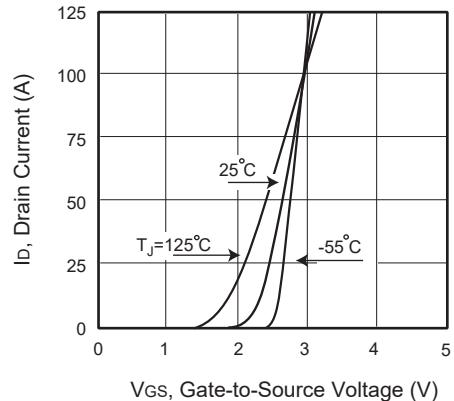


Figure 2. Transfer Characteristics

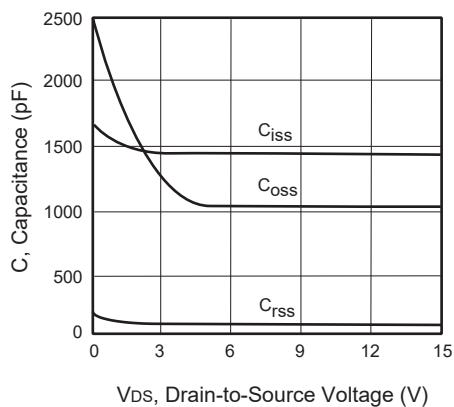


Figure 3. Capacitance

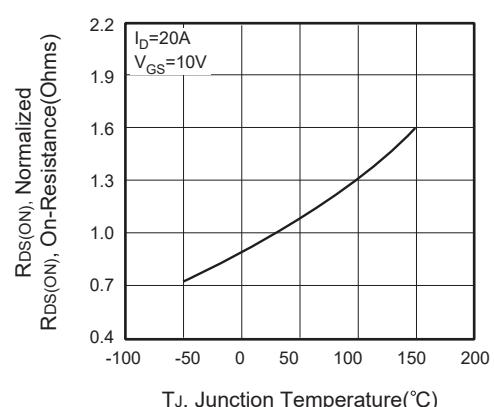


Figure 4. On-Resistance Variation with Temperature

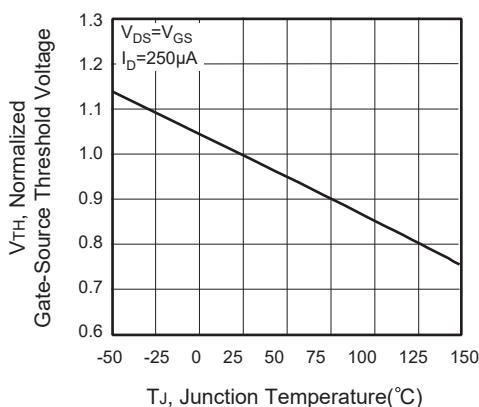


Figure 5. Gate Threshold Variation with Temperature

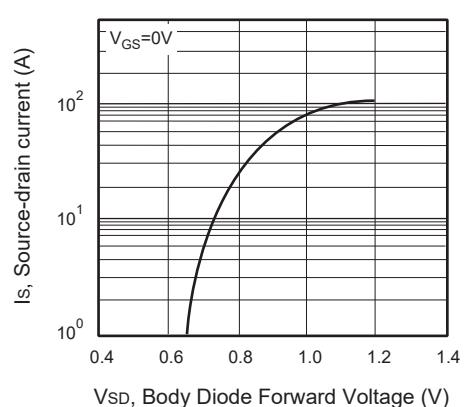


Figure 6. Body Diode Forward Voltage Variation with Source Current

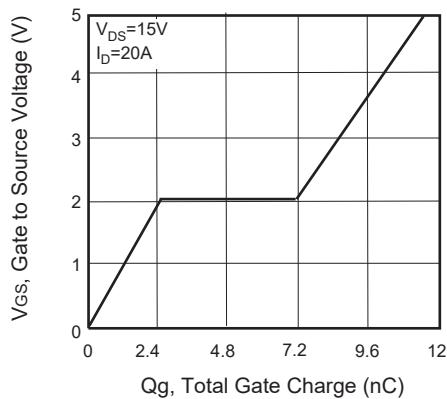


Figure 7. Gate Charge

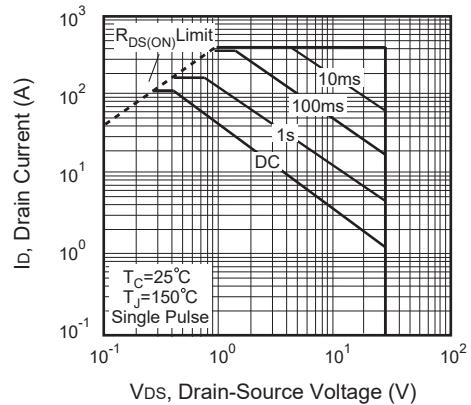


Figure 8. Maximum Safe Operating Area

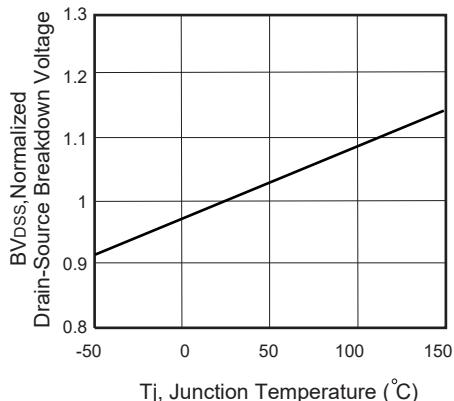


Figure 9. Breakdown Voltage Variation VS Temperature

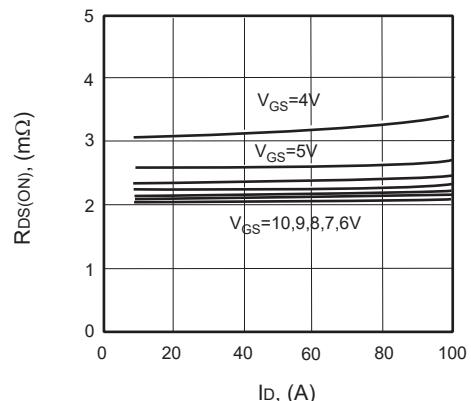


Figure 10. On-Resistance vs. Drain Current

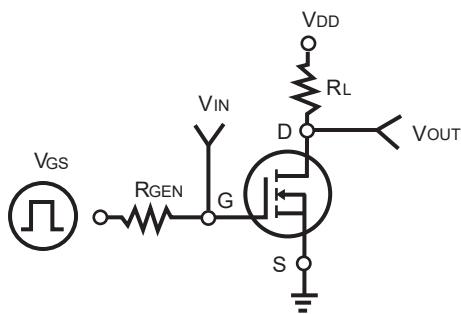


Figure 11. Switching Test Circuit

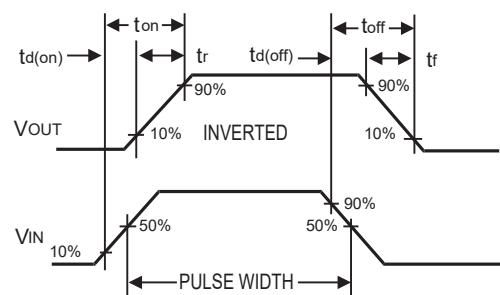


Figure 12. Switching Waveforms

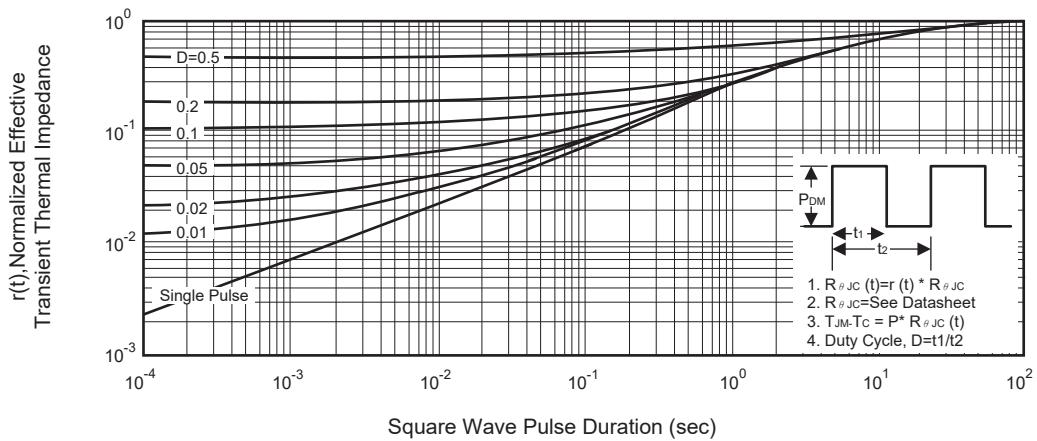
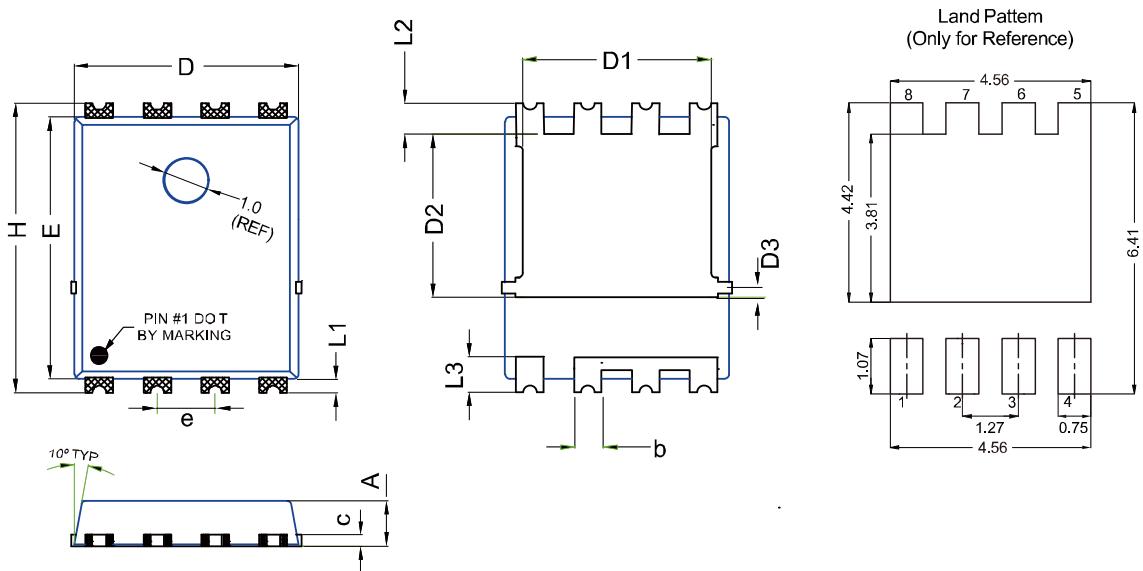


Figure 13 Normalized Thermal Transient Impedance Curve

P-PAK5X6 產品外觀尺寸圖 (Product Outline Dimension)
SINGLE PAD 尺寸圖


SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.900	1.100	0.035	0.043
b	0.300	0.500	0.012	0.020
c	0.154	0.354	0.006	0.014
D	4.800	5.200	0.189	0.205
D1	3.800	4.250	0.150	0.167
D2	3.570	3.970	0.141	0.156
D3	0.380	0.850	0.015	0.033
E	5.660	6.060	0.223	0.239
e	1.270 TYP		0.050 TYP	
H	6.250	6.450	0.246	0.254
L1	0.150	0.350	0.006	0.014
L2	0.580	0.780	0.023	0.031
L3	0.680	0.880	0.027	0.035